

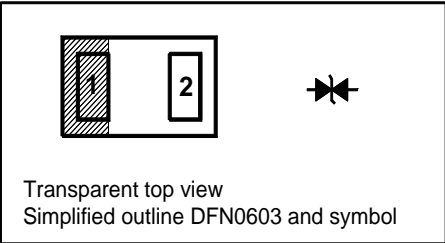
ESD Protection Diode

Features

- AEC-Q101 Qualified
- Low reverse current
- Low capacitance
- Halogen and Antimony Free(HAF), RoHS compliant

PINNING

PIN	DESCRIPTION
1	Anode
2	Anode



Absolute Maximum Ratings (T_a = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp = 8/20 μs)	P _{PK}	100	W
Peak Pulse Current (tp = 8/20 μs)	I _{PP}	4	A
ESD (IEC61000-4-2) Air Contact	V _{ESD}	± 25 ± 17	KV
Operating Junction Temperature Range	T _j	- 55 to + 125	°C
Storage Temperature Range	T _{stg}	- 55 to + 150	°C

Characteristics at T_a = 25°C

Parameter	Symbol	Min.	Max.	Unit
Reverse Stand-Off Voltage	V _{RWM}	-	5	V
Reverse Breakdown Voltage at I _R = 1 mA	V _{(BR)R}	6	11	V
Reverse Current at V _{RWM} = 5 V	I _R	-	100	nA
Clamping Voltage at I _{PP} = 1 A, tp = 8/20 μs at I _{PP} = 4 A, tp = 8/20 μs	V _C	- -	15 25	V
Junction Capacitance at V _R = 0 V, f = 1 MHz	C _j	-	0.5	pF

Electrical Characteristic Curves

Fig 1. Pulse Waveform

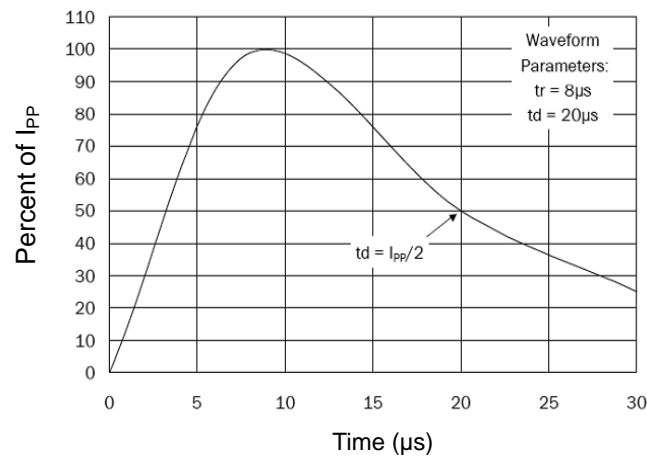


Fig 2. Power Derating Curve

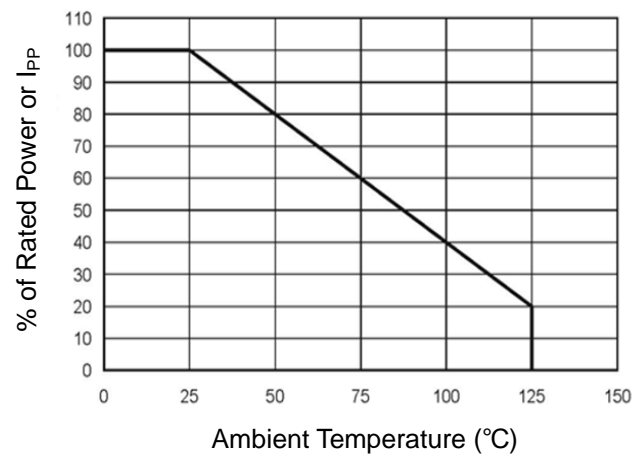


Fig 3. Clamping Voltage Curve

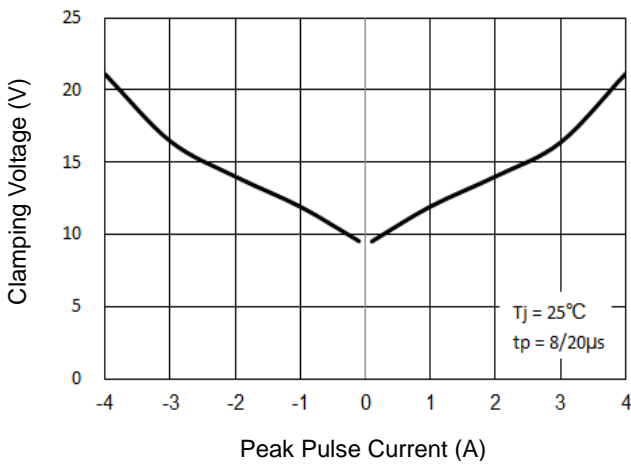


Fig 4. Junction Capacitance

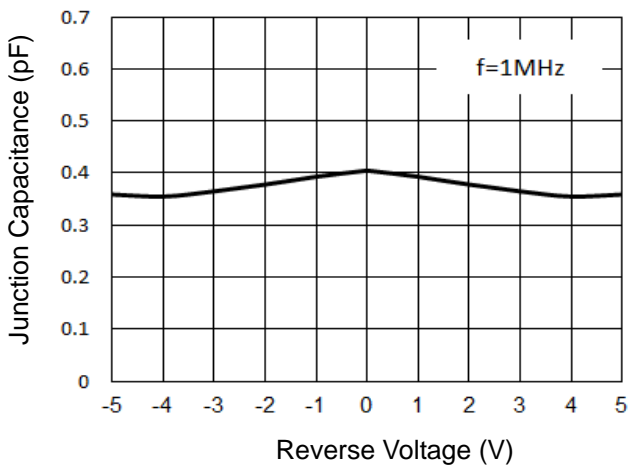
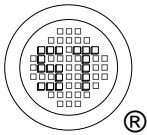
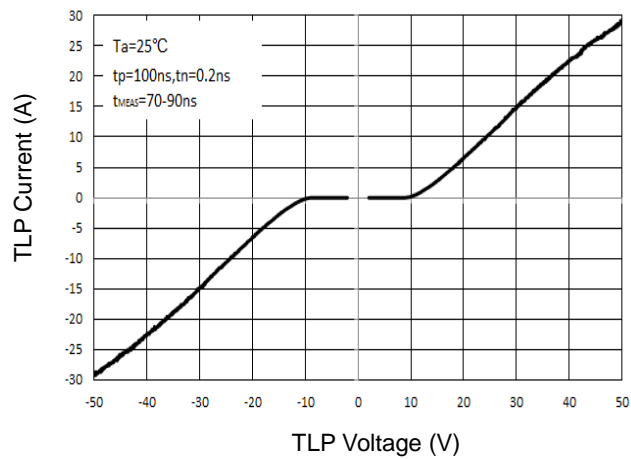


Fig. 5 TLP Curve

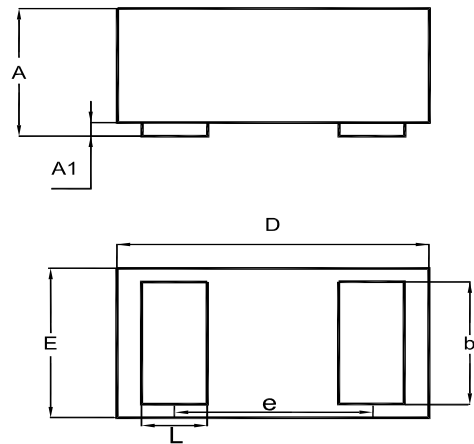


ESDBL0521Z-AH

PACKAGE OUTLINE

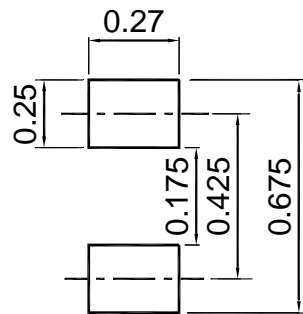
Plastic surface mounted package; 2 leads

DFN0603



UNIT	A	A1	b	D	E	e	L
mm	0.27 0.33	0 0.025	0.21 0.29	0.57 0.65	0.28 0.35	0.355	0.14 0.22

Recommended Soldering Footprint

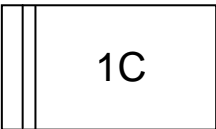


Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN0603	8	2 ± 0.1	0.079 ± 0.004	178	7	10,000

Marking information

" 1C " = Part No.
" II " = Marking Line
Font type: Arial



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